

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|--|---|------------------|---------|------------------|
| S1 | 486 | 362/555.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 17:47 |
| S2 | 189 | 362/228.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 17:47 |
| S3 | 646 | 362/231.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 17:47 |
| S4 | 131 | 362/230.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 17:47 |
| S5 | 1728 | 362/800.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 17:48 |
| S6 | 720 | 362/373.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 17:48 |
| S7 | 7988 | ("LED" or diode) and ("RGB") | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 17:54 |
| S8 | 1197 | S7 and ("PCB" or (circuit adj1 board)) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 17:55 |
| S9 | 876 | S8 and memory | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 17:50 |
| S10 | 847 | S9 and circuit | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 17:50 |
| S11 | 332 | S10 and (integrated adj1 circuit) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 17:55 |
| S12 | 69 | S11 and "CU" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 17:50 |

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|-----|------|--|---|----|-----|------------------|
| S13 | 36 | S12 and "AL" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 17:51 |
| S14 | 1 | S13 and "362"/\$.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 17:52 |
| S15 | 3 | ("10048373").PN. | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | OFF | 2005/09/23 17:53 |
| S20 | 222 | ("LED" or diode) and ("RGB") and "362"/\$.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 19:22 |
| S21 | 78 | S20 and ("PCB" or (circuit adj1 board)) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 19:23 |
| S22 | 16 | S21 and (integrated adj1 circuit) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 19:23 |
| S23 | 1601 | ("LED" or diode) and ("RGB" or (red and blue and green)) and "362"/\$.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 19:29 |
| S24 | 575 | S23 and ("PCB" or (circuit adj1 board)) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 19:30 |
| S25 | 99 | S24 and (integrated adj1 circuit) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 19:30 |
| S26 | 13 | S25 and 362/555.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 19:30 |
| S27 | 1384 | ("LED" or diode) and ("RGB" or (red and blue and green and (white or clear))) and "362"/\$.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 19:30 |
| S28 | 508 | S27 and ("PCB" or (circuit adj1 board)) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 19:30 |

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| S29 | 86 | S28 and (integrated adj1 circuit) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 19:30 |
| S30 | 12 | S29 and 362/555.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 19:31 |
| S31 | 16 | S29 and "Rgb" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 19:52 |
| S32 | 392 | "circuit board" and "RGB" and ((control adj1 unit) or "IC") and memory and ("LED" or diode) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 19:53 |
| S33 | 6 | S32 and "362"/\$.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 20:00 |
| S34 | 274 | 362/561.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 20:00 |
| S35 | 9 | S34 and "rgb" and (diode or "LED") | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/09/23 20:01 |